

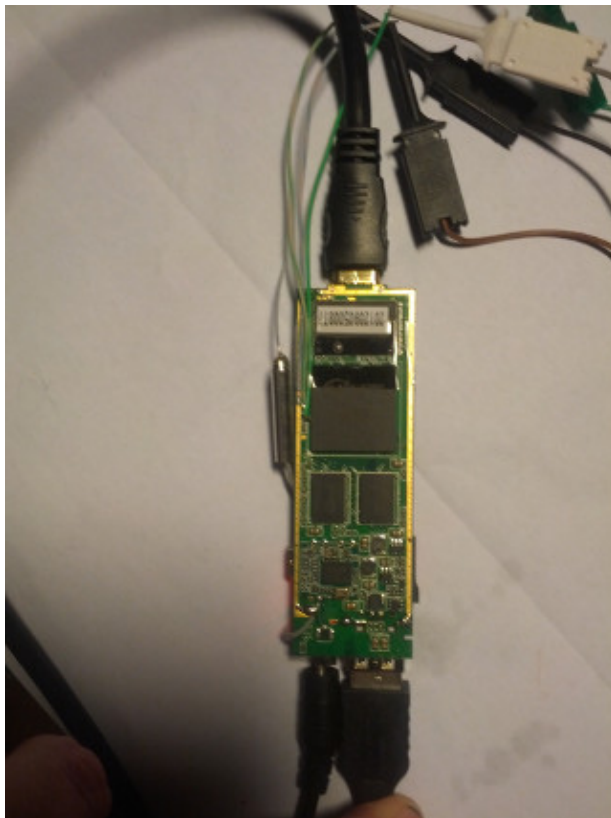
This page documents various things about this nice A10 machine with 1Gb RAM

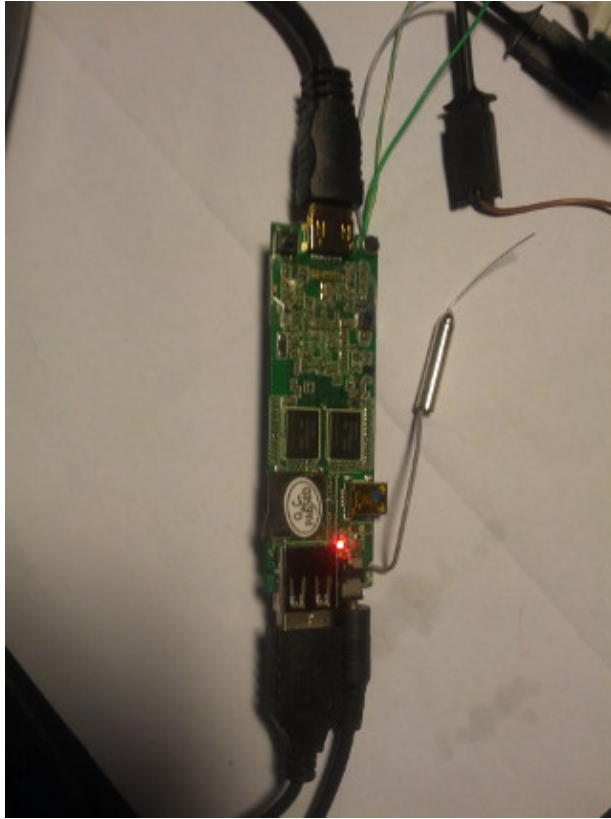
`t=0x8e65fc0 [0,0]`

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Serial port





Boot

- A10 boot overview - http://rhombus-tech.net/allwinner_a10/a10_boot_process/
- <https://github.com/hipboi/mksunxiboot>
- <https://github.com/linux-sunxi/u-boot-sunxi/wiki>

u-boot

<https://www.miniand.com/forums/forums/development--3/topics/uboot>

Kernel

(not really needed if u-boot detects memory correctly)

CONFIG_CMDLINE mem=1024M

Kernel compilation

<http://linux-sunxi.org/Linux>

```
make ARCH=arm CROSS_COMPILE=arm-linux-gnueabihf- sun4i_defconfig
make ARCH=arm CROSS_COMPILE=arm-linux-gnueabihf- -j16 uImage modules
make ARCH=arm CROSS_COMPILE=arm-linux-gnueabihf- INSTALL_MOD_PATH=output modules_install
```

Debian

- 512Mb <http://romanrm.ru/en/a10/debian>
- 1Gb <http://jas-hacks.blogspot.co.uk/2012/10/hackberry-a10-debian-whezzy-headless.html>

Android

Source

- https://github.com/oopsmonk/android_device_allwinner_mk802
- <https://github.com/oopsmonk/android/blob/jellybean/default.xml>

CyanogenMod 10

- <https://www.miniand.com/forums/forums/development/topics/cyanogenmod-10-full-working-with-inter>

Building

<https://github.com/linux-sunxi/sunxi-bsp>

<https://github.com/cnxsoft/a10-config>

MicroSD breakout

- http://linux-sunxi.org/MicroSD_Breakout

Pin	MicroSD	JTAG Connection	14-pin ARM JTAG Header	UART Connection	5-pin UART Header
1	Data2	TCK	9	nc	nc
2	CD/Data3	nc	nc	RX	2
3	Cmd	TDO	11	nc	nc
4	VDD	VTG	1,13	VDD	3
5	CLK	nc	nc	TX	1
6	VSS	GND	2,4,6,8,10,14	GND	4, 5
7	Data0	TDI	5	nc	nc
8	Data1	TMS	7	nc	nc
nc	nc	nTRST	3	nc	nc
nc	nc	nRESET	12	nc	nc