

[BusBlaster-v2.0a1.sch.landscape.pdf](#)

[http://dangerousprototypes.com/docs/Bus\\_Blaster\\_v2\\_design\\_overview](http://dangerousprototypes.com/docs/Bus_Blaster_v2_design_overview)

## Xilinx CoolRunner-II

I have two boards: v2.0a1 and v2.5

ta=0x0425538 [0, 0]

Contents: [Dobrica PavlinuÅiÄ 's random unstructured stuff]

- [Dobrica PavlinuÅiÄ 's random unstructured stuff \(UrJTAG\)](#)
- [Dobrica PavlinuÅiÄ 's random unstructured stuff \(Update CPLD logic\)](#)

# UrJTAG

```
root@blue:/bluez/FPGA/Altera-x300# jtag
```

```
UrJTAG 0.10 #2007
```

```
Copyright (C) 2002, 2003 ETC s.r.o.
```

```
Copyright (C) 2007, 2008, 2009 Kolja Waschk and the respective authors
```

UrJTAG is free software, covered by the GNU General Public License, and you are welcome to change it and/or distribute copies of it under certain conditions.

There is absolutely no warranty for UrJTAG.

warning: UrJTAG may damage your hardware!

Type "quit" to exit, "help" for help.

```
jtag> cable jtagkey vid=0x0403 pid=0x6010 interface=1
Connected to libftdi driver.
jtag> detect
IR length: 8
Chain length: 1
Device Id: 00000110111000011100000010010011 (0x06E1C093)
  Manufacturer: Xilinx (0x093)
  Part(0):      xc2c32a-vq44 (0x6E1C)
  Stepping:    0
  Filename:    /usr/share/urjtag/xilinx/xc2c32a-vq44/xc2c32a-vq44
Segmentation fault
# WTF?
```

So, compile urjtag with [libft2xx](#)

You must remove kernel modules:

```
jtag> cable ft2232 interface=1
error: Couldn't connect to suitable USB device.
error: ftdi/ftd2xx error: ftdi_usb_open_desc() failed: device not found
jtag> quit
```

```
root@blue:~# rmmmod ftdi_sio
```

```
root@blue:~# jtag
```

```
jtag> cable ft2232 interface=1
Connected to libftd2xx driver.
jtag> detect
IR length: 8
Chain length: 1
Device Id: 00000110111000011100000010010011 (0x06E1C093)
  Manufacturer: Xilinx (0x093)
  Part(0):      xc2c32a-vq44 (0x6E1C)
  Stepping:     0
  Filename:     /usr/local/share/urjtag/xilinx/xc2c32a-vq44/xc2c32a-vq44
```

## Update CPLD logic

[http://dangerousprototypes.com/docs/Bus\\_Blaster\\_buffer\\_logic](http://dangerousprototypes.com/docs/Bus_Blaster_buffer_logic)

```
root@blue:~# jtag
```

```
UrJTAG 0.10 #2051
Copyright (C) 2002, 2003 ETC s.r.o.
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```

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warning: UrJTAG may damage your hardware!  
Type "quit" to exit, "help" for help.

```
jtag> cable ft2232 interface=1
Connected to libftd2xx driver.
jtag> detect
IR length: 8
Chain length: 1
Device Id: 00000110111000011100000010010011 (0x06E1C093)
  Manufacturer: Xilinx (0x093)
  Part(0):      xc2c32a-vq44 (0x6E1C)
  Stepping:     0
  Filename:     /usr/local/share/urjtag/xilinx/xc2c32a-vq44/xc2c32a-vq44
jtag> svf /blue-zfs/dangerous-prototypes-open-hardware/Bus_Blaster/buffer_logic/BBv2-JTAGkey-v1.4
detail: Parsing      660/664 ( 99%)detail:
detail: Scanned device output matched expected TDO values.
```