

OBD to RS232 Interpreter

Description

Almost all new automobiles produced today are required, by law, to provide an interface from which test equipment can obtain diagnostic information. The data transfer on these interfaces follow several standards, none of which are directly compatible with PCs or PDAs. The ELM327 is designed to act as a bridge between these On-Board Diagnostics (OBD) ports and a standard RS232 interface.

The ELM327 takes the best features from our proven ELM320, ELM322, and ELM323 interfaces, and builds on them by adding seven CAN protocols, and a large number of commands. The result is an IC that can automatically sense and convert the most common OBD protocols in use today, while also providing high speed RS232 communications, battery voltage monitoring, customizable features, J1939 formatting, and a special low power mode, to name only a few.

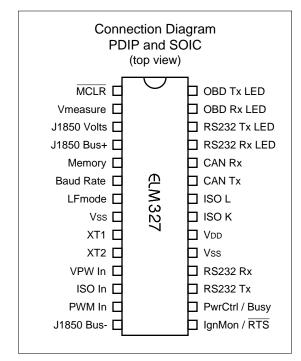
The ELM327 requires few external components to make a fully functioning circuit. The following pages discuss the interface details, and show how to use the IC to 'talk' to your vehicle, then conclude with two schematics to get you started.

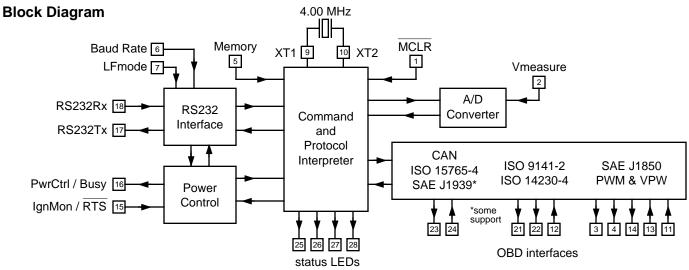
Applications

- Diagnostic trouble code readers
- Automotive scan tools
- Teaching aids

Features

- · Power Control with standby mode
- RS232 baud rates to 500Kbps
- · Automatically searches for protocols
- Fully configurable with AT commands
- · Voltage input for battery monitoring
- Low power CMOS design







Pin Descriptions

MCLR (pin 1)

A momentary (>2µsec) logic low applied to this input will reset the ELM327. If unused, this pin should be connected to a logic high (VDD) level.

Vmeasure (pin 2)

This analog input is used to measure a 0 to 5V signal that is applied to it. Care must be taken to prevent the voltage from going outside of the supply levels of the ELM327, or damage may occur. If it is not used, this pin should be tied to either VDD or Vss.

J1850 Volts (pin 3)

This output can be used to control a voltage supply for the J1850 Bus+ output. The pin will output a logic high level when a nominal 8V is required (for J1850 VPW), and will output a low level when 5V is needed (as for J1850 PWM applications). If this switching capability is not required for your application, this output can be left open-circuited.

J1850 Bus+ (pin 4)

This active high output is used to drive the J1850 Bus+ Line to an active level. Note that this signal does not have to be used for the Bus- Line (as was the case for the ELM320), since a separate J1850 Bus- drive output is provided on pin 14.

Memory (pin 5)

This input controls the default state of the memory option. If this pin is at a high level during power-up or reset, the memory function will be enabled by default. If it is at a low level, then the default will be to have it disabled. Memory can always be enabled or disabled with the AT M1 and AT M0 commands.

Baud Rate (pin 6)

This input controls the baud rate of the RS232 interface. If it is at a high level during power-up or

reset, the baud rate will be set to 38400 (or the rate that has been set by PP 0C). If at a low level, the baud rate will always be 9600.

LFmode (pin 7)

This input is used to select the default linefeed mode to be used after a power-up or system reset. If it is at a high level, then by default messages sent by the ELM327 will be terminated with both a carriage return and a linefeed character. If it is at a low level, lines will be terminated by a carriage return only. This behaviour can always be modified by issuing an AT L1 or AT L0 command.

Vss (pins 8 and 19)

Circuit common must be connected to these pins.

XT1 (pin 9) and XT2 (pin 10)

A 4.000 MHz oscillator crystal is connected between these two pins. Loading capacitors as required by the crystal (typically 27pF each) will also need to be connected between each of these pins and circuit common (Vss).

Note that this device has not been configured for operation with an external oscillator – it expects a crystal to be connected to these pins. Use of an external clock source is not recommended. Also, that this oscillator is turned off when in the Low Power or 'standby' mode of operation.

VPW In (pin 11)

This is the active high input for the J1850 VPW data signal. When at rest (bus recessive) this pin should be at a low logic level. This input has Schmitt trigger waveshaping, so no special amplification is required.

ISO In (pin 12)

This is the active low input for the ISO 9141 and

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Pin Descriptions (continued)

ISO 14230 data signal. It is derived from the K Line, and should be at a high logic level when at rest (bus recessive). No special amplification is required, as this input has Schmitt trigger waveshaping.

PWM In (pin 13)

This is the active low input for the J1850 PWM data signal. It should normally be at a high level when at rest (ie. bus recessive). This input has Schmitt trigger waveshaping, so no special amplification is required.

J1850 Bus- (pin 14)

This active high output is used to drive the J1850 Bus- Line to an active (dominant) level for J1850 PWM applications. If unused, this output can be left open-circuited.

IgnMon / RTS (pin 15)

This input pin can serve one of two functions, depending on how the Power Control options (PP 0E) are set.

If both bit 7 and bit 2 of PP 0E are '1's, this pin will act as an Ignition Monitor. This will result in a switch to the Low Power mode of operation, should the signal go to a low level, as would happen if the vehicle's ignition were turned off. An internal 'debounce' timer is used to ensure that the ELM327 does not shut down for noise at the input.

When the voltage at pin 15 is again restored to a high level, and a time of 1 or 5 seconds (as set by PP 0E bit 1) passes, the ELM327 will perform a 'Warm Start' and return to normal operation. A low to high transition at pin 15 will in fact restore normal operation, regardless of the setting of PP 0E bit 2, or whether pin 15 was the initial cause for the low power mode. This feature allows a system to control how and when it switches to low power standby operation, but still have automatic wakeup by the ignition voltage, or even by a pushbutton.

If either bit 7 or bit 2 of PP 0E are '0', this pin will function as an active low 'Request To Send' input. This can be used to interrupt the OBD processing in order to send a new command, or as previously mentioned, to highlight the fact that the ignition has been turned off. Normally kept at a high level, this input is brought low for attention, and should remain so until the Busy line (pin 16) indicates that the

ELM327 is no longer busy, or until a prompt character is received (if pin 16 is being used for power control).

This input has Schmitt trigger waveshaping. By default, pin 15 acts as the RTS interrupt input.

PwrCtrl / Busy (pin 16)

This output pin can serve one of two functions, depending on how the Power Control options (PP 0E) are set.

If bit 7 of PP 0E is a '1', this pin will function as a Power Control output. The normal state of the pin will be as set by PP 0E bit 6, and the pin will remain in that state until the ELM327 switches to the Low Power mode of operation, when the output switches to the opposite level. This output is typically used to control enable inputs, but may also be used for relay circuits, etc. with suitable buffering. The discussion on page 63 ('Modifications for Low Power Standby Operation') provides more details on how to use this output.

If bit 7 of PP 0E is a '0', pin 16 will function as a 'Busy' output, showing when the ELM327 is actively processing a command (the output will be at a high level), or when it is idle, ready to receive commands (the output will be low).

By default, bit 7 of PP 0E is '1', so pin 16 provides the Power Control function.

RS232Tx (pin 17)

This is the RS232 data transmit output. The signal level is compatible with most interface ICs (output is normally high), and there is sufficient current drive to allow interfacing using only a PNP transistor, if desired.

RS232Rx (pin 18)

This is the RS232 receive data input. The signal level is compatible with most interface ICs (when at idle, the level is normally high), but can be used with other interfaces as well, since the input has Schmitt trigger waveshaping.

VDD (pin 20)

This pin is the positive supply pin, and should always be the most positive point in the circuit. Internal circuitry connected to this pin is used to provide power on reset of the microprocessor, so an external



Pin Descriptions (continued)

reset signal is not required. Refer to the Electrical Characteristics section for further information.

ISO K (pin 21) and ISO L (pin 22)

These are the active high output signals which are used to drive the ISO 9141 and ISO 14230 buses to an active (dominant) level. Many new vehicles do not require the L Line – if yours does not, you can simply leave pin 22 open-circuited.

CAN Tx (pin 23) and CAN Rx (pin 24)

These are the two CAN interface signals that must be connected to a CAN transeiver IC (see the Example Applications section for more information). If unused, pin 24 should be connected to a logic high (VDD) level.

RS232 Rx LED (pin 25), RS232 Tx LED (pin 26), OBD Rx LED (pin 27) and OBD Tx LED (pin 28)

These four output pins are normally high, and are driven to low levels when the ELM327 is transmitting or receiving data. These outputs are suitable for directly driving most LEDs through current limiting resistors, or interfacing to other logic circuits. If unused, these pins may be left open-circuited.

Note that pin 28 can also be used to turn off all of the Programmable Parameters, if you can not do so by using the normal interface - see page 49 for details.

Ordering Information

These integrated circuits are 28 pin devices, available in either a 300 mil wide plastic ('skinny') DIP format or in a 300 mil SOIC surface mount type of package. We do not offer an option for QFN packages.

To order, add the appropriate suffix to the part number:

300 mil 28 pin Plastic DIP.....ELM327P

300 mil 28 pin SOIC......ELM327SM

Absolute Maximum Ratings

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Voltage on VDD with respect to Vss	0.3V to +7.5V
Voltage on any other pin with respect to Vss0.3V	to (V _{DD} + 0.3V)

Note:

These values are given as a design guideline only. The ability to operate to these levels is neither inferred nor recommended, and stresses beyond those listed here will likely damage the device.



Electrical Characteristics

All values are for operation at 25°C and a 5V supply, unless otherwise noted. For further information, refer to note 1 below.

Characteristic		Minimum	Typical	Maximum	Units	Conditions
Supply voltage, VDD		4.5	5.0	5.5	V	
V _{DD} rate of rise		0.05			V/ms	see note 2
Average current, IDD	normal		12		mA	ELM327 device only - does not include any load currents
	low power		0.15		mA	include any load currents
Input threshold voltage		0.8		2.0	٧	all, except Schmitt inputs
Schmitt trigger input thresholds	rising		2.9	4.0	٧	Pins 1, 11, 12, 13, 15 and 18 only
	falling	1.0	1.5		٧	
Output low voltage			0.3		٧	current (sink) = 10 mA
Output high voltage			4.4		٧	current (source) = 10 mA
Brown-out reset voltage		4.11	4.33	4.55	٧	
A/D conversion time			9		msec	see note 3
Minimum RS232 low lev wake the IC from Low P		128		-	µsec	
IgnMon debounce time		50	65		msec	
AT LP to PwrCtrl output time			1.0		sec	
LP ALERT to PwrCtrl ou	utput time		2.0		sec	

Notes:

- 1. This integrated circuit is based on Microchip Technology Inc.'s PIC18F2480 device. For more detailed device specifications, and possibly clarification of those given, please refer to the Microchip documentation (available at http://www.microchip.com/).
- 2. This spec must be met in order to ensure that a correct power on reset occurs. It is quite easily achieved using most common types of supplies, but may be violated if one uses a slowly varying supply voltage, as may be obtained through direct connection to solar cells or some charge pump circuits.
- 3. The time taken from when the AT RV command completes to when the voltage reading response begins.



AT Command Summary

OBD Commands

AL Allow Long (>7 byte) messages

AR Automatically Receive

ATO, 1, 2 Adaptive Timing Off, Auto1*, Auto2

BD perform a Buffer Dump

BI Bypass the Initialization sequence

DP Describe the current Protocol
DPN Describe the Protocol by Number

H0, H1 Headers Off*, or On

MA Monitor All

MR hh Monitor for Receiver = hhMT hh Monitor for Transmitter = hhNL Normal Length messages*

PC Protocol Close

R0, R1 Responses Off, or On*

RA hh set the Receive Address to hh **S0, S1** printing of Spaces Off, or On*

SH xyz Set Header to xyz
SH xxyyzz Set Header to xxyyzz

SP h
 SP Ah
 Set Protocol to h and save it
 SR hh
 Set the Receive address to hh
 use Standard Search order (J1978)

ST hh Set Timeout to hh x 4 msec
TA hh set Tester Address to hh

TP h Try Protocol h

TP Ah Try Protocol h with Auto search

ISO Specific Commands (protocols 3 to 5)

FI perform a Fast Initiation

IB 10 Set the ISO Baud rate to 10400*
IB 48 Set the ISO Baud rate to 4800
IB 96 Set the ISO Baud rate to 9600
IIA hh Set ISO (slow) Init Address to hh

KW display the Key Words

KW0, KW1 Key Word checking Off, or On*
 SI perform a Slow (5 baud) Initiation
 SW hh Set Wakeup interval to hh x 20 msec

WM [1 - 6 bytes] Set the Wakeup Message

J1850 Specific Commands (protocols 1 and 2)

IFR0, 1, 2 IFRs Off, Auto*, or On

IFR H, S IFR value from Header* or Source

CAN Specific Commands (protocols 6 to C)

CEA turn off CAN Extended Addressing
CEA hh use CAN Extended Address hh
CAF0, CAF1 Automatic Formatting Off, or On*

CF hhh set the ID Filter to hhh

CF hhhhhhhh set the ID Filter to hhhhhhhhh CFC0, CFC1 Flow Controls Off, or On* CM hhh set the ID Mask to hhh

CM hhhhhhh set the ID Mask to hhhhhhhh
CP hh set CAN Priority to hh (29 bit)
CRA hhh
set CAN Receive Address to hhh
CRA hhhhhhhh set the Rx Address to hhhhhhhh

CS show the CAN Status counts

D0, D1 display of the DLC Off*, or On

FC SM h Flow Control, Set the Mode to h

FC SH hhh FC, Set the Header to hhh **FC SH hhhhhhhh** Set the Header to hhhhhhhh

FC SD [1 - 5 bytes] FC, Set Data to [...]

PB xx yy Protocol B options and baud rate

RTR send an RTR message

V0, V1 use of Variable DLC Off*, or On

J1939 CAN Specific Commands (protocols A to C)

DM1 Monitor for DM1 messages
 JE use J1939 Elm data format*
 JS use J1939 SAE data format
 MP hhhh
 MP hhhhh
 Monitor for PGN 0hhhh
 MP hhhhhh

continued on the next page...



AT Command Summary (continued)

General Commands

<CR> repeat the last command BRD hh try Baud Rate Divisor hh BRT hh set Baud Rate Timeout set all to Defaults E0, E1 Echo Off, or On* FΕ Forget Events print the version ID L0, L1 Linefeeds Off, or On LP go to Low Power mode M0, M1 Memory Off, or On RD Read the stored Data SD hh Save Data byte hh

WS Warm Start (quick software reset)

Ζ reset all

@1 display the device description @2 display the device identifier

@3 ccccccccc

stores the @2 device identifier

Programmable Parameter Commands

PP xx OFF disable Prog Parameter xx PP FF OFF all Prog Parameters Off PP xx ON enable Prog Parameter xx PP FF ON all Prog Parameters On for PP xx, Set the Value to yy PP xx SV yy

print a PP Summary **PPS**

Voltage Reading Commands

CV dddd Calibrate the Voltage to dd.dd volts **CV 0000** Restore CV value to factory setting

RV Read the Voltage

Other

IGN read the IgnMon input level

> Settings shown with an asterisk (*) Note:

> > are the default values